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Patents

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hideyuki Andoh

Serial No. 10/013,087

Filed: December 7, 2001

For: Semiconductor Devices and the
Manufacturing Method of the Same

Art Unit: 2812

Examiner: Not yet assigned

BEST AVAILABLE CO.

Preliminary Amendment

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

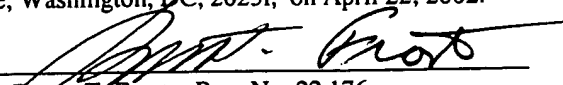
Response to the Notice of Omitted Items dated January 22, 2002 in the patent application identified above, please enter the following amendment and reconsider that notice in view of the appended remarks.

In the Specification

Please amend the paragraph beginning at page 11, line 1 as follows:

A1 Figs. 3A to 3H are process cross-sectional views showing a manufacturing method of the semiconductor device according to the embodiment of the present invention, respectively;

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Box Issue Fee, Washington, DC, 20231, on April 22, 2002.


Roger P. Frost - Reg. No. 22,176